

The listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Currently Amended) A display device comprising:

a pixel portion; and

a source signal line driving circuit comprising:

a first unit comprising:

a plurality of first latch circuits;

a plurality of first stages of a shift register which output pulses for outputting a signal in accordance with clock signals, wherein the pulses decide timing of latching of the plurality of first latch circuits; and

a current source configured to decide whether a current is outputted or not by the signal from the shift register; and

a plurality of first level shifter shifters for executing level conversion of signals inputted into the plurality of first latch circuits to which the current is supplied from the current source; and

a second unit comprising:

a plurality of second latch circuits;

a plurality of second stages of the shift register which output pulses in accordance with the clock signals, wherein the pulses decide timing of latching of the plurality of second latch circuits; and

a plurality of second level shifters for executing level conversion of signals inputted into the plurality of second latch circuits,

wherein the first unit is configured to stop supply of currents to the plurality of first level shifters while the plurality of second stages of the shift register output the pulses,
and

wherein the second unit is configured to stop supply of currents to the plurality of second level shifters while the plurality of first stages of the shift register output the pulses.

2. (Previously Presented) A display device according to claim 1, wherein said source signal line driving circuit and said pixel portion are provided over a member selected from the group consisting of a glass substrate, a plastic substrate, a stainless steel substrate and a single crystal wafer.

3. (Currently Amended) A display device according to claim 1, wherein said source signal line driving circuit and said pixel portion are provided over a same substrate.

4. (Currently Amended) A display device according to claim 1, wherein said source signal line driving circuit and said pixel portion are provided over different substrates.

5. (Previously Presented) A display device according to claim 1, wherein said display device is a liquid crystal display device.

6. (Previously Presented) A display device according to claim 1, wherein said display device is incorporated into a personal computer.

7. (Previously Presented) A display device according to claim 1, wherein said display device is incorporated into a portable information terminal.

8. (Previously Presented) A display device according to claim 1, wherein said display device is incorporated into a car audio set.

9. (Previously Presented) A display device according to claim 1, wherein said display device is incorporated into a digital camera.

10. (Currently Amended) A display device comprising:

a pixel portion; and

a source signal line driving circuit comprising:

first to x-th (x: natural number, $x \geq 2$) units, each of the first to x-th units comprising:

a plurality of latch circuits;

a plurality of stages of a shift register for outputting a signal which output pulses in accordance with clock signals, wherein the pulses decide timing of latching of the plurality of latch circuits; and

a current source configured to decide whether a current is outputted or not by the signal from the plurality of stages of the shift register; and

a plurality of level shifters to which the current is supplied from the current source for executing level conversion of signals inputted into the plurality of latch circuits,

wherein each of the first to x-th units is configured to stop supply of currents to the plurality of level shifters while the plurality of stages of the shift register in the other units output the pulses.

11. (Previously Presented) A display device according to claim 10, wherein said source signal line driving circuit and said pixel portion are provided over a member selected from the group consisting of a glass substrate, a plastic substrate, a stainless steel substrate and a single crystal wafer.

12. (Currently Amended) A display device according to claim 10, wherein said source signal line driving circuit and said pixel portion are provided over a same substrate.

13. (Currently Amended) A display device according to claim 10, wherein said source signal line driving circuit and said pixel portion are provided over different substrates.

14. (Previously Presented) A display device according to claim 10, wherein said display device is a liquid crystal display device.

15. (Previously Presented) A display device according to claim 10, wherein said display device is incorporated into a personal computer.

16. (Previously Presented) A display device according to claim 10, wherein said display device is incorporated into a portable information terminal.

17. (Previously Presented) A display device according to claim 10, wherein said display device is incorporated into a car audio set.

18. (Previously Presented) A display device according to claim 10, wherein said display device is incorporated into a digital camera.

19.-72. (Canceled)

73. (Currently Amended) A display device comprising:
a pixel portion; and
a source signal line driving circuit comprising:

a first unit comprising:

a plurality of first latch circuits;

a plurality of first stages of a decoder which output pulses for outputting a signal in accordance with input signals, wherein the pulses decide timing of latching of the plurality of first latch circuits; and

a current source configured to decide whether a current is outputted or not by the signal from the decoder; and

a plurality of first level shifter shifters for executing level conversion of signals inputted into the plurality of first latch circuits to which the current is supplied from the current source; and

a second unit comprising:

a plurality of second latch circuits;

a plurality of second stages of the decoder which output pulses in accordance with the input signals, wherein the pulses decide timing of latching of the plurality of second latch circuits; and

a plurality of second level shifters for executing level conversion of signals inputted into the plurality of second latch circuits,

wherein the first unit is configured to stop supply of currents to the plurality of first level shifters while the plurality of second stages of the decoder output the pulses, and

wherein the second unit is configured to stop supply of currents to the plurality of second level shifters while the plurality of first stages of the decoder output the pulses.

74. (Previously Presented) A display device according to claim 73, wherein said source signal line driving circuit and said pixel portion are provided over a member selected from the group consisting of a glass substrate, a plastic substrate, a stainless steel substrate and a single crystal wafer.

75. (Currently Amended) A display device according to claim 73, wherein said source signal line driving circuit and said pixel portion are provided over a same substrate.

76. (Currently Amended) A display device according to claim 73, wherein said source signal line driving circuit and said pixel portion are provided over different substrates.

77. (Previously Presented) A display device according to claim 73, wherein said display device is a liquid crystal display device.

78. (Previously Presented) A display device according to claim 73, wherein said display device is incorporated into a personal computer.

79. (Previously Presented) A display device according to claim 73, wherein said display device is incorporated into a portable information terminal.

80. (Previously Presented) A display device according to claim 73, wherein said display device is incorporated into a car audio set.

81. (Previously Presented) A display device according to claim 73, wherein said display device is incorporated into a digital camera.

82. (Currently Amended) A display device comprising:

a pixel portion; and

a source signal line driving circuit comprising:

first to x-th (x: natural number, $x \geq 2$) units, each of the first to x-th units comprising:

a plurality of latch circuits;

a plurality of stages of a decoder ~~for outputting a signal which~~
output pulses in accordance with input signals, wherein the pulses decide timing of
latching of the plurality of latch circuits; and

~~a current source configured to decide whether a current is~~
~~outputted or not by the signal from the plurality of stages of the decoder; and~~

a plurality of level shifters ~~to which the current is supplied from the~~
~~current source~~ for executing level conversion of signals inputted into the plurality of latch
circuits.

wherein each of the first to x-th units is configured to stop supply of currents to
the plurality of level shifters while the plurality of stages of the decoder in the other units
output the pulses.

83. (Previously Presented) A display device according to claim 82, wherein said source signal line driving circuit and said pixel portion are provided over a member selected from the group consisting of a glass substrate, a plastic substrate, a stainless steel substrate and a single crystal wafer.

84. (Currently Amended) A display device according to claim 82, wherein said source signal line driving circuit and said pixel portion are provided over a same substrate.

85. (Currently Amended) A display device according to claim 82, wherein said source signal line driving circuit and said pixel portion are provided over different substrates.

86. (Previously Presented) A display device according to claim 82, wherein said display device is a liquid crystal display device.

87. (Previously Presented) A display device according to claim 82, wherein said display device is incorporated into a personal computer.

88. (Previously Presented) A display device according to claim 82, wherein said display device is incorporated into a portable information terminal.

89. (Previously Presented) A display device according to claim 82, wherein said display device is incorporated into a car audio set.

90. (Previously Presented) A display device according to claim 82, wherein said display device is incorporated into a digital camera.

91.-144. (Canceled)

145. (Currently Amended) A semiconductor device comprising:

a first unit comprising:

a plurality of first latch circuits;

a plurality of first stages of a shift register which output pulses for outputting a signal in accordance with clock signals, wherein the pulses decide timing of latching of the plurality of first latch circuits; and

a current source configured to decide whether a current is outputted or not by the signal from the shift register; and

a plurality of first level shifter shifters for executing level conversion of signals inputted into the plurality of first latch circuits to which the current is supplied from the current source; and

a second unit comprising:

a plurality of second latch circuits;

a plurality of second stages of the shift register which output pulses in accordance with the clock signals, wherein the pulses decide timing of latching of the plurality of second latch circuits; and

a plurality of second level shifters for executing level conversion of signals inputted into the plurality of second latch circuits,

wherein the first unit is configured to stop supply of currents to the plurality of first level shifters while the plurality of second stages of the shift register output the pulses,
and

wherein the second unit is configured to stop supply of currents to the plurality of second level shifters while the plurality of first stages of the shift register output the pulses.

146. (Canceled)

147. (Previously Presented) A semiconductor device according to claim 145, wherein said semiconductor device is a liquid crystal display device.

148. (Previously Presented) A semiconductor device according to claim 145, wherein said display device is incorporated into a personal computer.

149. (Previously Presented) A semiconductor device according to claim 145, wherein said display device is incorporated into a portable information terminal.

150. (Previously Presented) A semiconductor device according to claim 145, wherein said display device is incorporated into a car audio set.

151. (Previously Presented) A semiconductor device according to claim 145, wherein said display device is incorporated into a digital camera.

152. (Currently Amended) A semiconductor device comprising:

first to x-th (x : natural number, $x \geq 2$) units, each of the first to x-th units comprising:

a plurality of latch circuits;

a plurality of stages of a shift register for outputting a signal which output pulses in accordance with clock signals, wherein the pulses decide timing of latching of the plurality of latch circuits; and

a current source configured to decide whether a current is outputted or not by the signal from the plurality of stages of the shift register; and

a plurality of level shifters for executing level conversion of signals inputted into the plurality of latch circuits,

wherein each of the first to x-th units is configured to stop supply of currents to the plurality of level shifters while the plurality of stages of the shift register in the other units output the pulses.

153. (Canceled)

154. (Previously Presented) A semiconductor device according to claim 152, wherein said semiconductor device is a liquid crystal display device.

155. (Previously Presented) A semiconductor device according to claim 152, wherein said display device is incorporated into a personal computer.

156. (Previously Presented) A semiconductor device according to claim 152, wherein said display device is incorporated into a portable information terminal.

157. (Previously Presented) A semiconductor device according to claim 152, wherein said display device is incorporated into a car audio set.

158. (Previously Presented) A semiconductor device according to claim 152, wherein said display device is incorporated into a digital camera.

159. (Currently Amended) A semiconductor device comprising:

~~a driving circuit comprising:~~

a first unit comprising:

a plurality of first latch circuits;

a plurality of first stages of a decoder which output pulses for outputting a signal in accordance with input signals, wherein the pulses decide timing of latching of the plurality of first latch circuits; and

~~a current source configured to decide whether a current is outputted or not by the signal from the plurality of stages of the decoder; and~~

a plurality of first level shifter shifters for executing level conversion of signals inputted into the plurality of first latch circuits to which the current is supplied from the current source; and

a second unit comprising:

a plurality of second latch circuits;

a plurality of second stages of the decoder which output pulses in accordance with the input signals, wherein the pulses decide timing of latching of the plurality of second latch circuits; and

a plurality of second level shifters for executing level conversion of signals inputted into the plurality of second latch circuits,

wherein the first unit is configured to stop supply of currents to the plurality of first level shifters while the plurality of second stages of the decoder output the pulses, and

wherein the second unit is configured to stop supply of currents to the plurality of second level shifters while the plurality of first stages of the decoder output the pulses.

160. (Canceled)

161. (Previously Presented) A semiconductor device according to claim 159, wherein said semiconductor device is a liquid crystal display device.

162. (Previously Presented) A semiconductor device according to claim 159, wherein said display device is incorporated into a personal computer.

163. (Previously Presented) A semiconductor device according to claim 159, wherein said display device is incorporated into a portable information terminal.

164. (Previously Presented) A semiconductor device according to claim 159, wherein said display device is incorporated into a car audio set.

165. (Previously Presented) A semiconductor device according to claim 159, wherein said display device is incorporated into a digital camera.

166. (Currently Amended) A semiconductor device comprising:

~~a driving circuit comprising:~~

first to x-th (x: natural number, $x \geq 2$) units, each of the first to x-th units comprising; comprising:

a plurality of latch circuits;

a plurality of stages of a decoder for outputting a signal which output pulses in accordance with input signals, wherein the pulses decide timing of latching of the plurality of latch circuits; and

~~a current source configured to decide whether a current is outputted or not by the signal from the plurality of stages of the decoder; and~~

~~a plurality of level shifters to which the current is supplied from the current source for executing level conversion of signals inputted into the plurality of latch circuits.~~

~~wherein each of the first to x-th units is configured to stop supply of currents to the plurality of level shifters while the plurality of stages of the decoder in the other units output the pulses.~~

167. (Canceled)

168. (Previously Presented) A semiconductor device according to claim 166, wherein said semiconductor device is a liquid crystal display device.

169. (Previously Presented) A semiconductor device according to claim 166, wherein said display device is incorporated into a personal computer.

170. (Previously Presented) A semiconductor device according to claim 166, wherein said display device is incorporated into a portable information terminal.

171. (Previously Presented) A semiconductor device according to claim 166, wherein said display device is incorporated into a car audio set.

172. (Previously Presented) A semiconductor device according to claim 166, wherein said display device is incorporated into a digital camera.

173. (Previously Presented) A display device according to claim 1, wherein said source signal line driving circuit comprises thin film transistors.

174. (Previously Presented) A display device according to claim 10, wherein said source signal line driving circuit comprises thin film transistors.

175.-176. (Canceled)

177. (Previously Presented) A display device according to claim 73, wherein said source signal line driving circuit comprises thin film transistors.

178. (Previously Presented) A display device according to claim 82, wherein said source signal line driving circuit comprises thin film transistors.

179.-188 (Canceled)